



AF/2184  
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Parson 3-2-1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): D.E. Parson et al.  
Case: 3-2-1-4  
Serial No.: 09/583,057  
Filing Date: May 30, 2000  
Group: 2184  
Examiner: Timothy M. Bonura

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: V. Benicewicz Date: December 10, 2003

Title: Control Method and Apparatus for Testing of Multiple Processor Integrated Circuits and Other Digital Systems

TRANSMITTAL LETTER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Technology Center 2100

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

- (1) Response to Office Action; and
- (2) Notice of Appeal.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error. A duplicate copy of the Notice of Appeal is enclosed.

Respectfully submitted,

Date: December 10, 2003

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